

## HE UNITED STATES PATENT AND TRADEMARK OFFICE

n re Application of:	)
Jeong Ho Park	) Group Art Unit: 2812
Application No.: 10/748,241	) Examiner: Isaac, Stanetta D.
Filed: December 31, 2003	) Confirmation No.: 6968
For: METHOD FOR FABRICATING TRANSISTOR OF SEMICONDUCTOR DEVICE	) ) )

## Mail Stop Amendment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

## <u>AMENDMENT</u>

In response to the Office Action mailed May 22, 2006, the period for response to which has been extended through September 22, 2006, by a Petition for Extension of one month, and fee payment submitted concurrently herewith, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims beginning on page 2 of this paper.

Remarks follow the Amendment to the Claims section, and begin on page 5 of this paper.